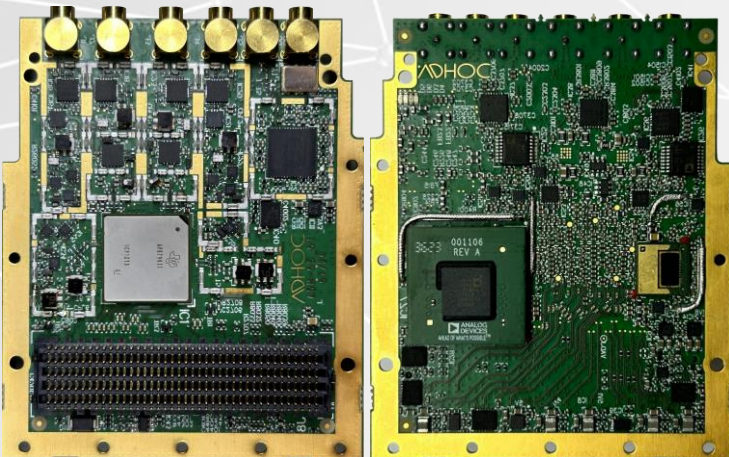


## Wideband AFE FPGA Mezzanine Card (FMC)



### Highlighted Features

- 2x2 MIMO 2Tx-2Rx or 2Tx/Rx Architecture
- 5 MHz - 2.5 GHz Operating RF Frequency
- Direct RF-Sampling ADCs and DACs
- Digitally Tunable Filters(DTF)
- High Gain LNAs with 1.1 dB NF
- Tx Output Power 20dBm ( $P_{1dB}$ : 30 dBm)

### Features

<b>RF Transceiver Unit</b>	<ul style="list-style-type: none"> <li>• AFE7903</li> <li>• Industrial Oper. Temp. -40 to 85°C)</li> <li>• Dual 14-bit DAC at 12 Gbps</li> <li>• Dual 14-bit ADC at 3 Gbps</li> <li>• Up to 400MHz IBW per Rx/Tx Channel</li> <li>• 40 dB, 0.125dB steps Internal DSA on Tx</li> <li>• 25dB, 0.5dB steps Internal DSA on Rx</li> </ul>
<b>Filters</b>	<ul style="list-style-type: none"> <li>• 30-90 MHz / 90-225 MHz/ 225-520MHz/ Bypass on Rx1 Channel</li> <li>• 520-1300 MHz / 1250-2600 MHz / Bypass on Rx2 Channel</li> <li>• Ceramic 2.5GHz Low Pass Filters at Rx and Tx channels</li> </ul>
<b>Low Noise Amplifiers</b>	<ul style="list-style-type: none"> <li>• GaAs MMIC LNA typ. 1.1 dB NF and 19.5 dB typ. gain</li> <li>• Two selectable stage on Rx channels</li> </ul>
<b>Integrated Power Amplifiers</b>	<ul style="list-style-type: none"> <li>• GaAs MMIC PA 25 dB typ. gain</li> <li>• Output <math>P_{1dB}</math>: 30 dBm</li> </ul>
<b>Analog Monitoring</b>	<ul style="list-style-type: none"> <li>• RSSI at RX connector and RFIC stage on each RX channels</li> <li>• TSSI on each Tx channel</li> </ul>
<b>Dynamic Port Switching</b>	<ul style="list-style-type: none"> <li>• Constant Impedance SP2T RF Switch on Rx and Tx channels</li> <li>• Supports different antenna topologies</li> </ul>
<b>Clock</b>	<ul style="list-style-type: none"> <li>• Internal VCXO (tj typ. 83fs @12KHz-20MHz)</li> <li>• Synchronization with External Clock serves multiple AFE boards</li> </ul>

### Interfaces

<b>RF Connectors</b>	<ul style="list-style-type: none"> <li>• SMP RF Connectors</li> <li>• Rx1</li> <li>• Tx1/Rx1</li> <li>• Rx2</li> <li>• Tx2/Rx2</li> <li>• EXT_CLK_IN</li> <li>• EXT_CLK_OUT</li> </ul>
<b>VITA 57.1 FMC HPC</b>	<ul style="list-style-type: none"> <li>• 8 Lane JESD204B/C up to 29.5 Gbps,</li> <li>• VADJ-3V3 Level Translated I/O 48 LA port Control I/O</li> <li>• VADJ-3V3 Level Translated I/O 4 HA port Control I/O</li> <li>• 2K EEPROM and Temperature Sensor</li> <li>• JTAG 3V3 Compatible</li> </ul>
<b>Power</b>	<ul style="list-style-type: none"> <li>• 3A at 3.3V and 1A at 12V supplied from Carrier Board as defined by ANSI/VITA 57.1</li> </ul>

### Mechanical

<b>Size</b>	• 69 mm x 84 mm (2.72" x 3.31")
<b>Mounting</b>	• Ruggedized
<b>Cooling</b>	• Conduction Cooled

#### Address

Üniversiteler Mah. 1605. Cadde, Bina No:3/1, E Blok  
No: 101

06800 Çankaya/Ankara, Türkiye

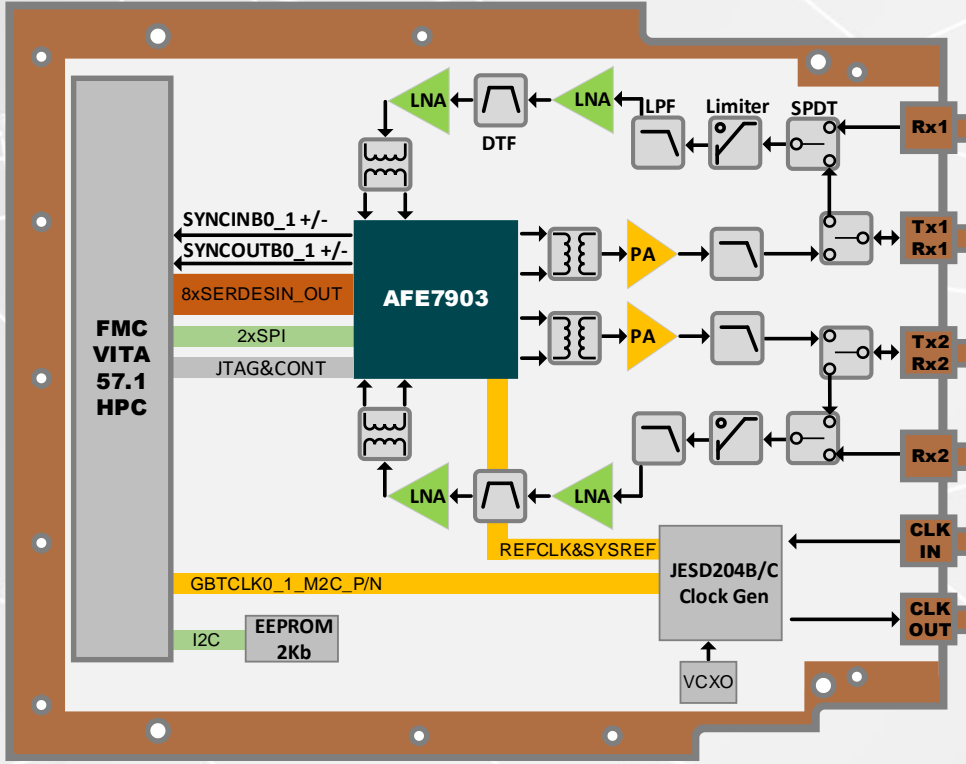
#### Contacts

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## Target Applications

- Flexible Architecture for Wide Range of Applications
- Optimal Software-Defined-Radio Front-End for Multiple Waveforms
- Multi-functional Radio Equipment like Cognitive Radio
- Reliable Communication (Diversity)
- Prototyping for Mass Production Solutions
- Satellite Communication with BUC and LNB
- Microwave Backhaul Systems
- Wireless Signal Processing and Analysis



## Support Services

- Highly motivated and skilled engineering team for custom development
- Contact us for IP solutions

## Customization

- Optimized custom design according to the desired operating frequency range
- Optimized custom design according to specific mechanics
- Fully compatible with the **ADHOC Teknoloji VIMK Board** as the carrier FPGA board

Adhoc Teknoloji A.Ş. is a startup engineering company located in Ankara, Türkiye. The company specializes in high-performance communication solutions, driven by its highly motivated engineering team.

In addition to design services, Adhoc Teknoloji A.Ş. is currently focusing on its High Frequency Trading Solutions, which include FPGA-based Tick-to-Trade systems, Accelerators, Market Data Simulators, Pre-Trade Risk Checks and Precision Network Measurement & Analysis tools.

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